

1. (Original) A method of activating a display element of a display device having  $n \times m$  array of display elements, each display element coupled to a logic controlled switch, the method comprising:

applying a row address input and a row electrode input to control logic of the logic controlled switch of the display element;

applying a column address input and a column electrode input to the control logic of the logic controlled switch of the display element;

activating the display element with the logic controlled switch when the row address and row electrode inputs and when the column address and column electrode inputs satisfy a condition.

2. (Currently Amended) The method of Claim 1,  
comparing the row address input and the row electrode input,  
comparing the column address input and the column electrode input,

activating the display [~~pixel~~] element with the logic controlled switch based on results of the comparisons.

3. (Original) The method of Claim 2, controlling the logic-controlled switch includes enabling and disabling the logic controlled switch with a charging capacitor.

4. (Original) The method of Claim 1,  
activating at least some display elements of the display device at a first refresh rate,

activating other display elements of the display device at a second refresh rate, different than the first refresh rate.

5. (Currently Amended) A method in a display device comprising an  $n \times m$  array of addressable display elements, the method comprising:

activating at least some display elements characterizing a foreground image at a first rate;

activating other display elements characterizing a background image at a second rate,

the second [~~refresh~~] rate less than the first [~~refresh~~] rate;

activating the display elements with a corresponding logic controlled display element switch when row address and row electrode inputs and when the column address and column electrode inputs satisfy a condition.

Claim 6 (Cancelled).

7. (Currently Amended) The method of Claim 6,  
comparing the row address input and the row electrode input,  
comparing the column address input and the column electrode input,

activating the display element with the logic controlled display element switch using the results of the comparisons.

8. (Currently Amended) The method of Claim 7, enabling and disabling the logic controlled display element switch with a switch enabling charging capacitor [~~gate~~] controlled by the results of the comparisons.

9. (Original) The method of Claim 5, activating other display elements at the second rate includes not activating the other display elements.

10. (Original) A display device comprising:  
a plurality of display elements arranged in a matrix,  
each display element including a display pixel coupled to a switch,  
each display element including an addressable latch having an output coupled to a controlling input of the switch,  
the addressable latch having a row address input and a column address input.

11. (Original) The device of Claim 10, the addressable latch having a row electrode input and a column electrode input.

12. (Original) The device of Claim 10,  
the addressable latch of each display element including row address logic and column address logic having corresponding outputs coupled to the output of the addressable latch,  
the row address input coupled to the row address logic, the column address input coupled to the column address logic.

13. (Original) The device of Claim 10,  
the addressable latch of each display element including first and second comparators, the first comparator having the row address input and a

row electrode input, the second comparator having the column address input and a column electrode input,

each display element including a logic device having a first input coupled to an output of the corresponding first comparator, the logic device having a second input coupled to an output of the corresponding second comparator.

14. (Original) The device of Claim 13, the logic device is an AND gate, the output of the addressable latch is an output the logic device

15. (Original) The device of Claim 13, a pixel capacitor connected parallel with the display pixel, and a switch enabling capacitor coupled to an input of the switch.

16. (Original) The device of Claim 10 is a thin-film-transistor display device.

17. (Currently Amended) A method in a display device comprising an  $n \times m$  array of addressable display elements, the method comprising:

selectively activating display elements by individually addressing the display elements to be activated, activating the display elements includes,

applying a row address input and a row electrode input to control logic of the corresponding display element,

applying a column address input and a column electrode input to the control logic of the corresponding display element, and

activating the display element with a logic controlled switch when the control logic inputs satisfy a condition;

reducing power consumption by addressing at least some of the display elements at a first frequency and addressing other display elements at a second frequency, the second frequency less than the first frequency.

Claim 18 (Canceled).

19. (Currently Amended) The method of Claim 17 [~~18~~],  
comparing the row address input and the row electrode input with the control logic,  
comparing the column address input and the column electrode input with the control logic,  
activating the display element by enabling the logic controlled switch using the results of the comparisons.

20. (Original) The method of Claim 19, enabling and disabling the logic controlled switch with a switch enabling capacitor controlled by the control logic.